

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

William Schmitz
Erik Paulsen
Gabriel Romero



Serial No. : 09/882,786

Group Art Unit : 2858

Filed : June 15, 2001

Examiner : Benson, Walter

For : Testing Implementation For Signal
Characterization

Atty Docket : 1003-0559 / 00-573

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

9/21/04 *Connie Del Castillo*
Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 9-17-04

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331